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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLAIMS	IND CLAIMS
10/561,732	04/25/2008	2811	1030	GB030097US1	15	1

CONFIRMATION NO. 7709

65913  
NXP, B.V.  
NXP INTELLECTUAL PROPERTY DEPARTMENT  
M/S41-SJ  
1109 MCKAY DRIVE  
SAN JOSE, CA 95131

FILING RECEIPT



Date Mailed: 05/22/2008

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

**Applicant(s)**

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**Assignment For Published Patent Application**

KONINKLIJKE PHILIPS ELECTRONICS N.V., Eindhoven, NETHERLANDS

**Power of Attorney:** The patent practitioners associated with Customer Number 24738

**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/IB2004/001992 06/10/2004

**Foreign Applications**

UNITED KINGDOM 0314390.6 06/20/2003

**If Required, Foreign Filing License Granted:** 05/16/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/561,732**

**Projected Publication Date:** 08/28/2008

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Lateral Field-Effect Transistor Having an Insulated Trench Gate Electrode

**Preliminary Class**

257

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